

<b>Notice of References Cited</b>	Application/Control No. 09/751,194	Applicant(s)/Patent Under Reexamination CATHEY ET AL.	
	Examiner Tri H. Phan	Art Unit 2661	Page 1 of 1

**U.S. PATENT DOCUMENTS**

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	B	US-6,157,955 A	12-2000	Narad et al.	709/228
	C	US-6,628,653 B1	09-2003	Salim, Jamal Hadi	370/389
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	M	US-			

**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Brown et al., 'An IRAM-Based Architecture for a Single-Chip ATM Switch', TR-07-97, Center for Research in Computing Technology, Harvard University, Cambridge, Massachusetts, pp 1-32.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.